

D4

9/5/3

001510529

WPI Acc No: 76-H3457X/197633

Digital data recording circuit - has additional buffer
storage reducing asynchronous data losses

Patent Assignee: GOLUBEV V I (GOLU-I)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
SU 491980	A	19760204					197633 B

Priority Applications (No Type Date): SU 2007991 A 19740326

Abstract (Basic): SU 491980 A

Circuit contains code converter (1), controls (2), magnetic recording unit (3), input register (4), buffer storage units (5, 6) with address counters (7, 8) and buffer storage operation control commutator (9). Incoming data is applied to buffer storage through input register (4) with address counters sorting out recording sequence. Code converter forms multidigit data and control codes. Data loss is prevented using additional buffer storage and a commutator which at convenient moments and as a need arises (e.g. overflow) changes over from one storage to another to secure uniform data flow for recording.

Title Terms: DIGITAL; DATA; RECORD; CIRCUIT; ADD; BUFFER; STORAGE; REDUCE; ASYNCHRONOUS; DATA; LOSS

Derwent Class: T03

International Patent Class (Additional): G11B-005/02

File Segment: EPI

DERWENT WPI (Dialog® File 351): (c)1999 Derwent Info Ltd. All rights reserved.

© 1998 The Dialog Corporation plc